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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/715133-Conf. #1894
				Filing Date	November 17, 2003
				First Named Inventor	Kaushik Saha
				Art Unit	2193
				Examiner Name	T. Mai
Sheet	1	of	1	Attorney Docket Number	64476-00003USPX

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	A1	6260053	07-10-2001	Kan Zhao	
	A2	6308191	10-23-2001	Gay-Bellile	
	A3				

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
	B1	EP 0818740	01-14-1998	Sun Microsystems		
	B2	EP 0285316	10-05-88	Tektronix		

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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials [*]	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
	C1	Dick "The Scalability of Linear Filters on Hypercube Concurrent Computers", Signals, Systems and Computers 1994, IEEE Comput. Soc. US., October 31 1994, pages 1069-1073		
	C2	Wilkenson "Digital System Design," 1987, Pretiss-Hall International, pages 460-463 and 472-473		
	C3	Datasheet – TMS320C30 Digital Signal Processor, Texas Instruments, 1997		
	C4	Slump "Design and Implementation of a Linear-phase Equalizer in Digital Audio Signal Processing": IEEE Workshop on VLSI Signal Processing V, October 28, 1992, pages 297-306		
	C5	Mou "Fast FIR Filtering: Algorithms and Implementations" Signal Processing, Amsterdam, NL, vol. 13, December 1987 pages 377-384		
	C6	European Search Report, for EP 03104135, dated July 14, 2004.		
	C7	Eyre, "The Evolution of DSP Processors" IEEE Signal Processing Magazine, March 2000		
	C8	Serneck, "The Evolution of DSP Architectures: Towards Parallelism Exploitation" 10 th Mediterranean Electrotechnical Conference, 2000, vol. II pages 782785.		
	C9	Mou "A Study of VLSI Symmetric FIR Filter Structures" Journal of VLSI Signal Processing 4, 1992 November.		
	C10	Deka, "A Comprehensive Study of Digital Signal Processing Devices" Microprocessors and Microsystems, vol. 19, no. 4 May 1995.		

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